## HEP Experiments: Fixed-Target and Collider



### Stages of Data Reconstruction



# Global Methods: Conformal Mapping + Histogramming

Global methods are especially suitable for fast tracking in projections

Histogram: **Conformal Mapping:** Transform circles into straight lines Collect a histogram of azimuth angles  $\phi$  $u = x/(x^2+y^2)$ Find peaks in the histogram  $v = -y/(x^2+y^2)$ Collect hits into tracks Simple Fast 20.0 18.0 16.0 78 1 2 14.0 12.0 -10.0 8.0 6.0 4.0 2.0 y 0.0--3.50 -2.50 -1.50 -0.50 0.50 1.50 2.50 3 50

Triggers

## Global Methods: Conformal Mapping + Histogramming



#### **Advantages:**

- Impressive visual simplification of the problem
- Each step is easy to implement in hardware
- This results in a fast algorithm

#### **Disadvantages:**

- Non-obvious complications of the problem
- Reverse order of the hits (last <-> first)
- Measurement errors are now no more uniform
- Geometry of detectors must be transformed
- Geometry of material walls must also be transformed
- What with the alignment constants ?
- A (non-uniform) magnetic field (map) must be transformed
- What with the Lorentz force: **F** = q(**E**+**v**x**B**) ?
- Needs to know exact position of the interaction point
- Finds only primary tracks
- Does not find secondary tracks
- Is it possible to build a trigger on primary tracks only ?
- In fact, histogramming provides only track parameters
- No errors of track parameters estimates (covariance matrix)
- No hits grouping into track candidates
- Therefore, no possibility to refit tracks
- Histogramming needs access to main memory (slow)

Conclusion: Useful implemented in hardware and for very simple event topologies only

# **Global Methods: Hough Transformation**





## **Global Methods: Hough Transformation**



**Conclusion:** Useful implemented in hardware and for simple event and trigger topologies

# Local Methods: Kalman Filter for Track Finding



## Local Methods: Kalman Filter for Track Finding



#### **Advantages:**

• ...

- Psychologically easy to accept hit by hit track finding
- Combined track finder and fitter based on KF
- Development of a new experiment starts with an ideal MC track finder and a realistic KF track fitter, therefore the next step to a realistic track finder is obvious – KF

#### **Disadvantages:**

- Track finding a combinatorial (NP) problem, can not be solved directly using methods suitable for single track
- Repeats the same calculations many times, when discarding track candidates
- Works at the hit level
- Needs seeding (starting short track segments)
- Final efficiency is always limited by seeding efficiency
- It is limited also by the efficiency of the seeding chambers
- Therefore needs a lot of seeds -> even larger combinatorics
- How many inefficient detectors can be tolerated in general ?
- How to include missing hits into the Kalman filter ?
- How to calculate chi^2 in this case ?
- Too early competition between track candidates
- ----

Conclusion: Useful for relatively simple event topologies and as initial (second after the ideal) track finder

## Cellular Automaton (CA) as Track Finder

Track finding: Which hits in detector belong to the same track? - Cellular Automaton (CA)



11 September 2012, GSI

### Cellular Automaton (CA) as Track Finder



#### **Advantages:**

- Local relations -> simple calculations
- Local relations -> parallel algorithm
- Staged implementation: hits -> segments -> tracks
- Polynomial (2nd order?) combinatorics
- Track competition at the global level
- Includes the KF fitter, if necessary, for high track densities
- Detector inefficiency problem outside the combinatorics
- ...

#### **Disadvantages:**

- Not easy to understand a parallel algorithm (Game of Life)
- Currently implementations on sequential computers
- Parallel hardware is coming now

• ...

Conclusion: Useful for complicated event topologies with large combinatorics and for parallel hardware

### Kalman Filter (KF) based Track Fit

Track fit: Estimation of the track parameters at one or more hits along the track – Kalman Filter (KF)



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## Coming now: Many-core Era of HPC



S. Borkar et al. (Intel), "Platform 2015: Intel Platform Evolution for the Next Decade", 2005.

✓ GP CPU

Intel: KNF

CPU/GPU

AMD: Fusion

? FPGA Xilinx: Virtex



- On-line event selection
- Mathematical and computational optimization
- Optimization of the detector

- Heterogeneous systems of many cores
- Uniform approach to all CPU/GPU families
- Similar programming languages (CUDA, ArBB, OpenCL)
- Parallelization of the algorithm (vectors, multi-threads, many-cores)

#### 11 September 2012, GSI

## Many-Core HPC: Cores, Threads and SIMD

HEP experiments work with high data rates, therefore need High Performance Computing (HPC) !



### Many-Core CPU/GPU Architectures



Optimized for low-latency access to cached data setsControl logic for out-of-order and speculative execution

**Intel MIC** 

#### **NVIDIA/AMD GPU**



More transistors dedicated to computation



• Many Integrated Cores architecture announced at ISC10 (June 2010)

- Based on the x86 architecture
- Many-cores + 4-way multithreaded + 512-bit wide vector unit

#### **IBM Cell**



- General purpose RISC processor (PowerPC)
- 8 co-processors (SPE, Synergistic Processor Elements)
- 128-bit wide SIMD units

Future systems are heterogeneous

## **CPU/GPU Programming Frameworks**



- Intel Ct (C for throughput), ArBB (Array Building Blocks)
- Extension to the C language
- Intel CPU/GPU specific
- SIMD exploitation for automatic parallelism
- NVIDIA CUDA (Compute Unified Device Architecture)
- Defines hardware platform
- Generic programming
- Extension to the C language
- Explicit memory management
- Programming on thread level
- OpenCL (Open Computing Language)
- Open standard for generic programming
- Extension to the C language
- Supposed to work on any hardware
- Usage of specific hardware capabilities by extensions

#### • Vector classes (Vc)

- Overload of C operators with SIMD/SIMT instructions
- Uniform approach to all CPU/GPU families
- Uni-Frankfurt/FIAS/GSI

ArBB, Vector classes: Cooperation with Intel

<u>54</u>	Stage	Description	Time/track	Speedup	
		Initial scalar version	12  ms	_	
	1	Approximation of the magnetic field	$240~\mu{\rm s}$	50	
불 く	2	Optimization of the algorithm	$7.2~\mu{ m s}$	35 >	10000x faster
Ξ (	3	Vectorization	$1.6~\mu{ m s}$	4.5 J	
} ख	4	Porting to SPE	$1.1 \ \mu s$	1.5	
	5	Parallelization on 16 SPEs	$0.1~\mu{ m s}$	10	
		Final simulized version	$0.1~\mu{ m s}$	120000	

Comp. Phys. Comm. 178 (2008) 374-383



blade11bc4 @IBM, Böblingen: 2 Cell Broadband Engines, 256 kB LS, 2.4 GHz The KF speed was increased by 5 orders of magnitude



Motivated by, but not restricted to Cell !

### **CBM Cellular Automaton Track Finder**



# Track Finding at Low Track Multiplicity



Au+Au mbias events at 25 AGeV, 8 STS, 0 x 7,5 strip angles

A minimum bias event: average reconstructed track multiplicity 109

2nd CBM Software Workshop, Ebernburg, 05.12.2012<sub>18</sub> /09

# Track Finding at Medium Track Multiplicity



A central event: average reconstructed track multiplicity 572

# Track Finding at High Track Multiplicity



A group with 100 minimum bias events: average reconstructed track multiplicity 10340

2nd CBM Software Workshop, Ebernburg, 05.12.201220 /09

# CA Track Finder: Efficiency and Time vs. Track Multiplicity



Stable reconstruction efficiency and time as a second order polynomial up to 100 minimum bias events in a group

### **KFParticle: Reconstruction of Vertices and Decayed Particles**



### **KFParticle Finder for Physics Analysis and Selection**



11 September 2012, GSI

### Standalone First Level Event Selection (FLES) Package



Given n threads each filled with 1000 events, run them on specified n logical cores, 1 thread per 1 core.



The FLES package shows strong scalability on up to 80 cores

### **Consolidate Efforts: Common Reconstruction Package**



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### Consolidate Efforts: International Workshops

### International Workshop for Future Challenges in Tracking and Trigger Concepts

1 <sup>st</sup>	GSI, Darmstadt, Germany,	07-11.06.2010;
2 <sup>nd</sup>	CERN, Geneva, Switzerland,	07-08.07.2011;
3 <sup>rd</sup>	FIAS, Frankfurt, Germany,	27-29.02.2012;
4 <sup>th</sup>	CERN, Geneva, Switzerland,	28-30.11.2012.

11 September 2012, GSI

